



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani

Art Unit : 2815

Serial No. : 09/389,393

Examiner : Jay Jackson

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Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of the claims that begins on page 3 of this paper.

**Amendments to the Drawings** begin on page 12 of this paper.

**Remarks/Arguments** begin on page 13 of this paper.

**An Appendix** with amended drawing figures, including an attached annotated sheet showing changes, is attached following page 13 of this paper.